Attorney's Docket No.: 10417-119001 / F51-143213M/SW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Yoshinori Hino et al.	Art Unit	: 2815
Serial No.	: 10/080,831	Examiner	: J. Nguyen
Filed	: February 21, 2002		
Title	: SEMICONDUCTOR DEVICE AND PATTERN LAYOUT METHOD THEREOF		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	14	-	20	=	0		$0
Independent	4	-	3	=	1		$86
First Presentation of Multiple Dependent Claims							$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 1 month
extension of time. $110

TOTAL FEE DUE $196

A check for $196 is attached.

Respectfully submitted,

Date:_____7|8|04_____

Samuel Borodach (signature)
Samuel Borodach
Reg. No. 38,388

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